

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,664,888 B2  
APPLICATION NO. : 10/694729  
DATED : February 16, 2010  
INVENTOR(S) : Tran et al.

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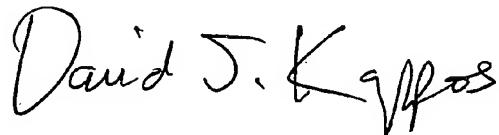
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At column 16, line 4 (claim 36), “(F) receiving data by least one of said first parallel part and” should read --(F) receiving data by at least one of said first parallel port and--.

At column 16, line 7 (claim 37), “(F) sending data from said first parallel part to at least one” should read --(F) sending data from said first parallel port to at least one--.

Signed and Sealed this

Thirtieth Day of November, 2010



David J. Kappos  
*Director of the United States Patent and Trademark Office*